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F8AF:AA      254      TAX          ;save ACC in X
F8B0:84 2A    255      STY  BAS2L    ;and Y in scrolling temp
F8B2:A0 10    256      LDY  #S10     ;call = finish mnemonics
F8B4:4C B4 FB 257      JMP  GOTOCX   ;off to C100
F8B7:        258 *
F8B7:        259 * Test slot 3 for a card containing ROM.
F8B7:        260 * If there is one, we'll not switch in our internal
F8B7:        261 * slot 3 firmware (for 80 columns).
F8B7:        262 * On entry Y has a high value like $F2, so the
F8B7:        263 * ROM/bus is read a bunch of times
F8B7:        264 *
F8B7:8D 06 C0 265      TSTROM STA  SLOTXROM ;swap in slots
F8BA:A2 02    266      TSTROM LDX  #2      ;check 2 ID bytes
F8BC:BD 05 C3 267      TSTROM1 LDA  $C305,X ;at C305 and $C307
F8BF:DD 9C FC 268      CMP  CLREOL,X ;with two bytes that are same
F8C2:D0 07 F8CB 269      BNE  XTST
F8C4:CA      270      DEX
F8C5:CA      271      DEX          ;check next ID byte
F8C6:10 F4 F8BC 272      BPL  TSTROM1
F8C8:88      273      DEY
F8C9:D0 EF F8BA 274      BNE  TSTROM    ;if ROM ok, exit with BEQ
F8CB:8D 07 C0 275      XTST STA  INTXROM ;swap internal ROM
F8CE:60      276      RTS          ;and return there
F8CF:        277 *
F8CF:EA      278      NOP          ;line things up
F8D0:        279 *
F8D0:20 82 F8 280      INSTDSP JSR  INSDS1   ;GEN FMT, LEN BYTES
F8D3:48      281      PHA          ;SAVE MNEMONIC TABLE INDEX
F8D4:B1 3A    282      PRNTOP LDA  (PCL),Y
F8D6:20 DA FD 283      JSR  PRBYTE
F8D9:A2 01    284      LDX  #S01      ;PRINT 2 BLANKS
F8DB:20 4A F9 285      PRNTBL JSR  PRBL2
F8DE:C4 2F    286      CPY  LENGTH   ;PRINT INST (1-3 BYTES)
F8E0:C8      287      INY          ;IN A 12 CHR FIELD
F8E1:90 F1 F8D4 288      BCC  PRNTOP
F8E3:A2 03    289      LDX  #S03      ;CHAR COUNT FOR MNEMONIC INDEX
F8E5:C0 04    290      CPY  #S04
F8E7:90 F2 F8DB 291      BCC  PRNTBL
F8E9:68      292      PLA          ;RECOVER MNEMONIC INDEX
F8EA:A8      293      TAY
F8EB:B9 C0 F9 294      LDA  MNEML,Y
F8EE:85 2C    295      STA  LMNEM      ;FETCH 3-CHAR MNEMONIC
F8F0:B9 00 FA 296      LDA  MNEMR,Y   ; (PACKED INTO 2-BYTES)
F8F3:85 2D    297      STA  RMNEM
F8F5:A9 00    298      PRMN1 LDA  #S00
F8F7:A0 05    299      LDY  #S05
F8F9:06 2D    300      PRMN2 ASL  RMNEM   ;SHIFT 5 BITS OF CHARACTER INTO A
F8FB:26 2C    301      ROL  LMNEM
F8FD:2A      302      ROL  A          ; (CLEARS CARRY)
F8FE:88      303      DEY
F8FF:D0 F8 F8F9 304      BNE  PRMN2
F901:69 BF    305      ADC  #SBF      ;ADD "?" OFFSET
F903:20 ED FD 306      JSR  COUT      ;OUTPUT A CHAR OF MNFM
F906:CA      307      DEX

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F907:D0 EC F8F5 308      BNE  PRMN1
F909:20 48 F9 309      JSR  PRBLNK   ;OUTPUT 3 BLANKS
F90C:A4 2F    310      LDY  LENGTH
F90E:A2 06    311      LDX  #S06      ;CNT FOR 6 FORMAT BITS
F910:E0 03    312      PRADRI CPX  #S03
F912:F0 1C F930 313      BEQ  PRADR5   ;IF X=3 THEN ADDR.
F914:06 2E    314      PRADR2 ASL  FORMAT
F916:90 0E F926 315      BCC  PRADR3
F918:BD B3 F9 316      LDA  CHAR1-1,X
F91B:20 ED FD 317      JSR  COUT
F91E:BD B9 F9 318      LDA  CHAR2-1,X
F921:F0 03 F926 319      BEQ  PRADR3
F923:20 ED FD 320      JSR  COUT
F926:CA      321      PRADR3 DEX
F927:D0 E7 F910 322      BNE  PRADRI
F929:60      323      RTS
F92A:88      324      PRADR4 DEY
F92B:30 E7 F914 325      BMI  PRADR2
F92D:20 DA FD 326      JSR  PRBYTE
F930:A5 2E    327      PRADR5 LDA  FORMAT
F932:C9 E8    328      CMP  #S08      ;HANDLE REL ADR MODE
F934:B1 3A    329      LDA  (PCL),Y   ;SPECIAL (PRINT TARGET,
F936:90 F2 F92A 330      BCC  PRADR4   ; NOT OFFSET)
F938:20 56 F9 331      RELADR JSR  PCADJ3
F938:AA      332      TAX          ;PCL,PCH+OFFSET+1 TO A,Y
F93C:E8      333      INX
F93D:D0 01 F940 334      BNE  PRNTYX   ;+1 TO Y,X
F93F:C8      335      INY
F940:98      336      PRNTYX TYA
F941:20 DA FD 337      PRNTAX JSR  PRBYTE   ;OUTPUT TARGET ADR
F944:8A      338      PRNTX  TXA      ; OF BRANCH AND RETURN
F945:4C DA FD 339      JMP  PRBYTE
F948:        340 *
F948:A2 03    341      PRBLNK LDX  #S03   ;BLANK COUNT
F94A:A9 A0    342      PRBL2 LDA  #S00   ;LOAD A SPACE
F94C:20 ED FD 343      PRBL3 JSR  COUT   ;OUTPUT A BLANK
F94F:CA      344      DEX
F950:D0 F8 F94A 345      BNE  PRBL2   ;LOOP UNTIL COUNT=0
F952:60      346      RTS
F953:        347 *
F953:38      348      PCADJ SEC          ;0=1 BYTE, 1=2 BYTE,
F954:A5 2F    349      PCADJ2 LDA  LENGTH ; 2=3 BYTE
F956:A4 3B    350      PCADJ3 LDY  PCH
F958:AA      351      TAX          ;TEST DISPLACEMENT SIGN
F959:10 01 F95C 352      BPL  PCADJ4   ; (FOR REL BRANCH)
F95B:88      353      DEY          ;EXTEND NEG BY DECR PCH
F95C:65 3A    354      PCADJ4 ADC  PCL
F95E:90 01 F961 355      BCC  RTS2      ;PCL+LENGTH(OR DISPL)+1 TO A
F960:C8      356      INY          ; CARRY INTO Y (PCH)
F961:60      357      RTS2 RTS
F962:        358 ;
F962:        359 ; FMT1 BYTES: XXXXXYO INSTRS
F962:        360 ; IF Y=0 THEN LEFT HALF BYTE
F962:        361 ; IF Y=1 THEN RIGHT HALF BYTE

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